



Agenda

# Testing Overview

HIL Test Bench: CANoe + VT System

Testing Design: vTESTstudio

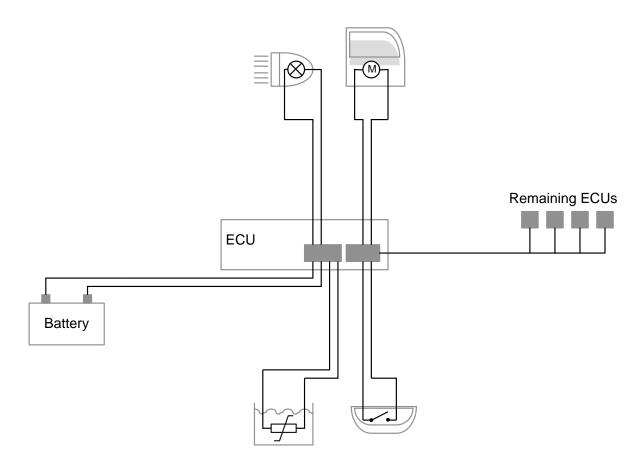
Test Data Management: vTESTcenter

**Testing Engineering Services** 

Summary



# What do I need for Testing an ECU?

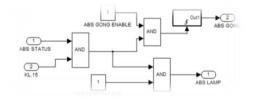




### What should be tested for ECU?

#### Application Layer Test

- Functionality logic test
  - Working mode
  - Control behavior
  - > .....



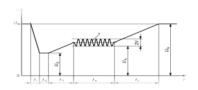
Diagnostic functionality test(DTCs)

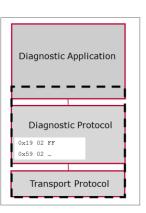
#### Electrical Test

- > Over voltage or Under voltage test
- > Starting profile
- > ...

#### System Integration Test

- > ECUs Interaction Behaviors
- > ...





#### **Communication Test**

- Physical Layer
  - > CAN/LIN Bus output voltage level
  - > DUT Ground shift tolerance
  - > Electric capacity and resistance characteristics
  - > .....

#### Data Link Layer

- > Bit timing parameters
- > DLC check
- > Cycle time check
- > ......

#### Interaction Layer

- Message transmission(e.g. Periodic/on event/if active)
- > Default value check

#### Network Management Test

- > Establish ring
- > Bus sleep & wake up
- > Loss of node in a running network test

#### Diagnostic Protocol Test

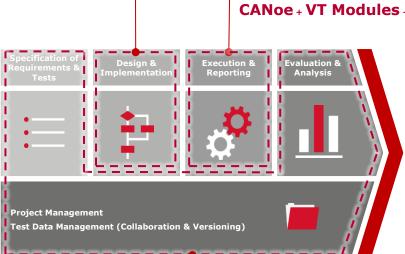
- > P2,P2\*
- > ..



### **Overview Vector Test Solution**

#### **vTESTstudio**

- Test programming (CAPL, C#)
- ► Table based test design
- Graphical test design (diagrams)
- Definition of parameters & curves



#### CANoe + VT Modules + Bus Interfaces

- Realtime execution of tests
- Access to SUT via
  - > I/Os,
  - > bus systems and
  - > protocols (diagnostics, XCP, ...)
  - > Debug Interface
- ▶ Detailed automatic test reporting

#### **vTESTcenter**

- ▶ Management of testing projects: Planning & Tracking
- Configuration management of test data
- ► Team collaboration by multi-user support
- Requirements and test engineering

- ▶ Bidirectional traceability
- Review & rework
- Import of test exec. reports
- ▶ High-level analysis of test results and trends



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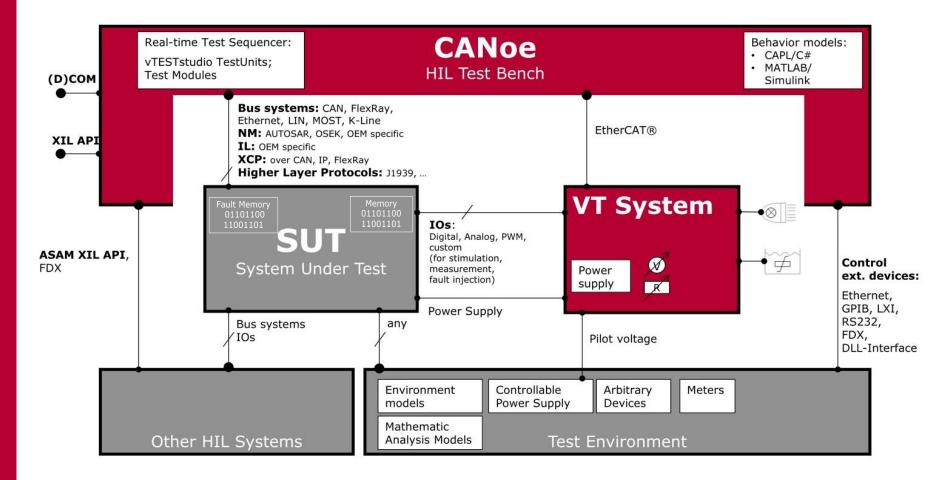
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**Testing Engineering Services** 

Summary

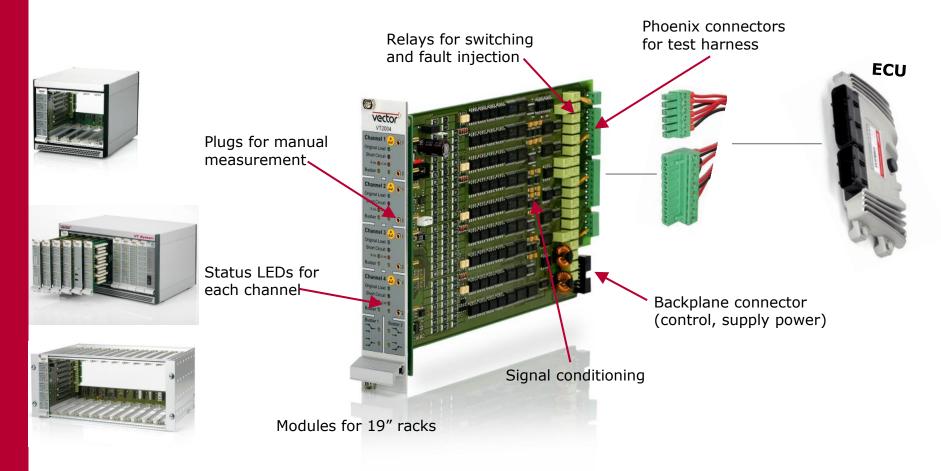


## Served SUT Interfaces, Served Interfaces of HIL test bench





## VT System – A Modular System



### HIL Test Bench: CANoe + VT System





**Stimulation Modules** analog VT2004A(FPGA) digital VT2516A(FPGA)



**General Purpose Modules** analog VT2816(FPGA)

digital VT2848(FPGA) Relais VT2820



Extension Module VT7900



Real-Time Modules
ATOM VT6010
Core™ i7 VT6051A

Network Interface Module VT6104 VT6204



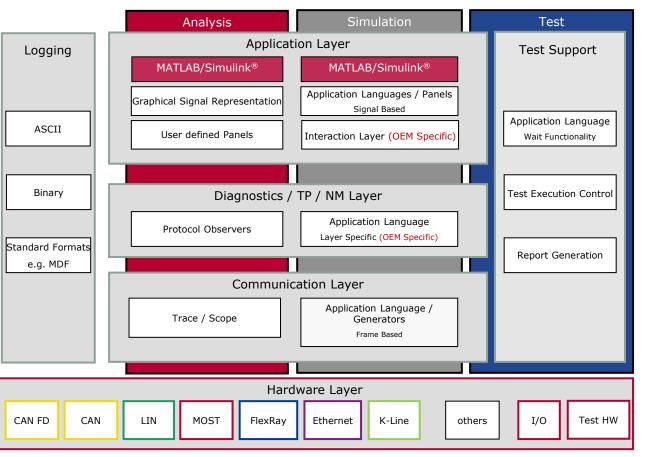
Power Module VT7001A



Backplanes & Chassis VT8006 VT8012



## Open Environment CANoe





VN5610



VT1004: Load and Measurement Module (Channel 3)

Integration Time

- - X

Cycle Time

VTS Configuration

Terminal 15 Terminal30

PowerSupplyInt

PowerSupply1 PowerSupply2

- Power

- Liahts1

Config ▼ | Module ▼ | Adapt to Connected HW...

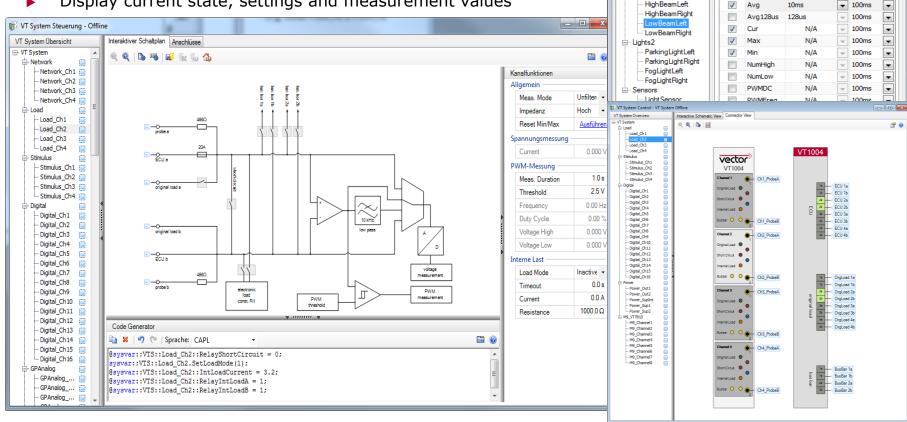
Left side low beam light.

Values Constraints

Name

### Fully CANoe Integration

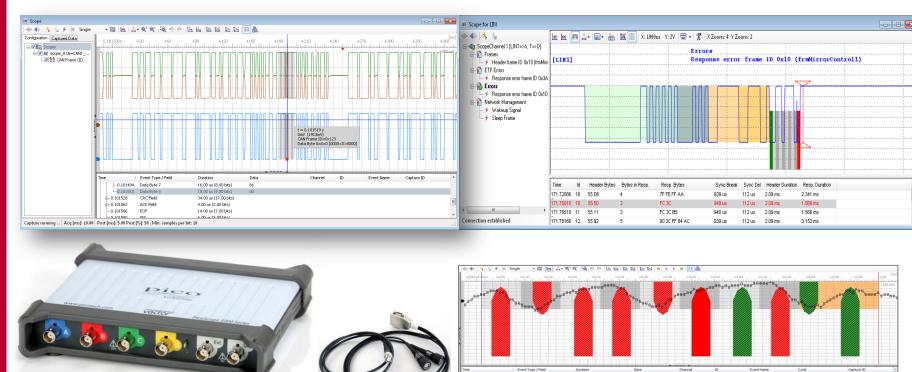
- Graphical user interface to control all VT module settings
- Relay switching apply directly on the schematic + code generation
- Display current state, settings and measurement values





#### CANoe.SCOPE

- Decode/analysis of bus voltage signals: CAN, CAN-FD, FlexRay, LIN
- External triggering via sync line of bus interface
- ▶ Typical use cases: protocol error analysis, <u>automated physical layer tests</u>



26.30 us (265.00 bits

26.30 us (265.00 bits) 26.30 us (265.00 bits)

252.30 us (2525.00 bits)

FR Frame Error

00 00 00 00 00 00 00 00 ... FlexRay1 A

00 00 00 00 00 00 00 00 ... FlexRay1 A 00 00 00 00 00 00 00 00 ... FlexRay1 A 00 03 05 55 FF FF FF FF ... FlexRay1 A

00 00 00 00 00 00 00 00 .... FlexRay 1 /

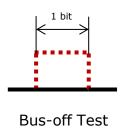
VehDyn\_Stat\_FR2 PresfCtrl\_Rq\_ESP\_OBD\_St...

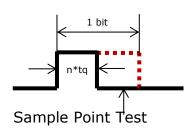
WhiLtStat\_VehAccebRs1\_F...



### **CANstress**

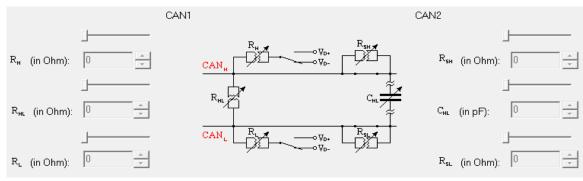
▶ Disturb on CAN protocol level(Digital) – Bit level & BTL(Bit Timing Logic=tq) cycles







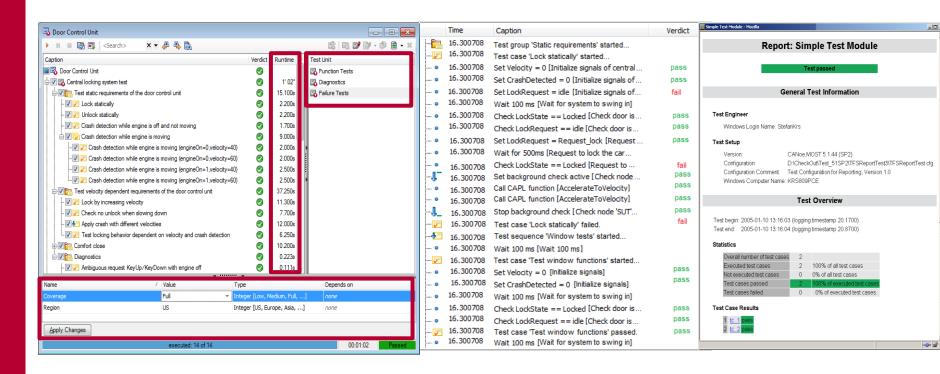
Disturb on CAN physical level(Analog)





#### Test Run with CANoe

- Possibility to observe and analyze a test during the test run in the Test Trace Window
- Detailed information about executed test cases and test steps
- Automatic Report Generation





## Data Logger

## GL1000 Family

- Compact fleet logger for 2 CAN, 2 LIN and I/Os
- GL1010 as waterproof IP65 variant



### ▶ GL2000

- Handy fleet logger for 4 CAN, 2 LIN and I/Os
- Fast wake-up to record the first message
- Slim and cost-efficient GPS receiver



## GL3000/GL4000 Family

- Flexibly expandable multi bus fleet logger for 9 CAN, 2 LIN, 2 FlexRay, 1 MOST150 and I/Os
- 2 independent logging memories for separate configuration
- High storage capacities on CF card, SSD or external USB hard disk
- Wireless data exchange via WiFi





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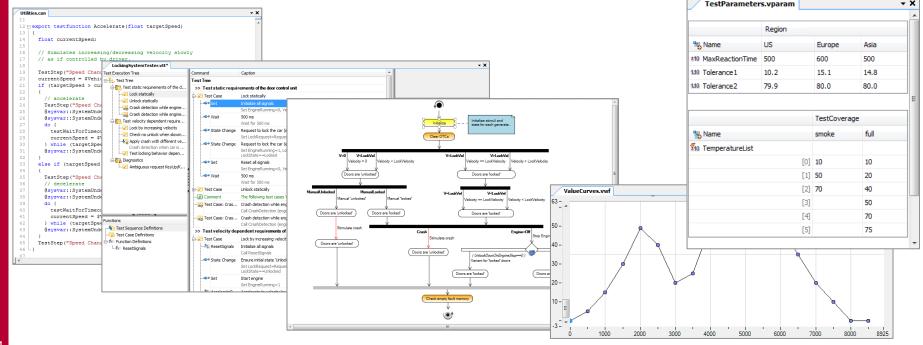
**Testing Engineering Services** 

Summary



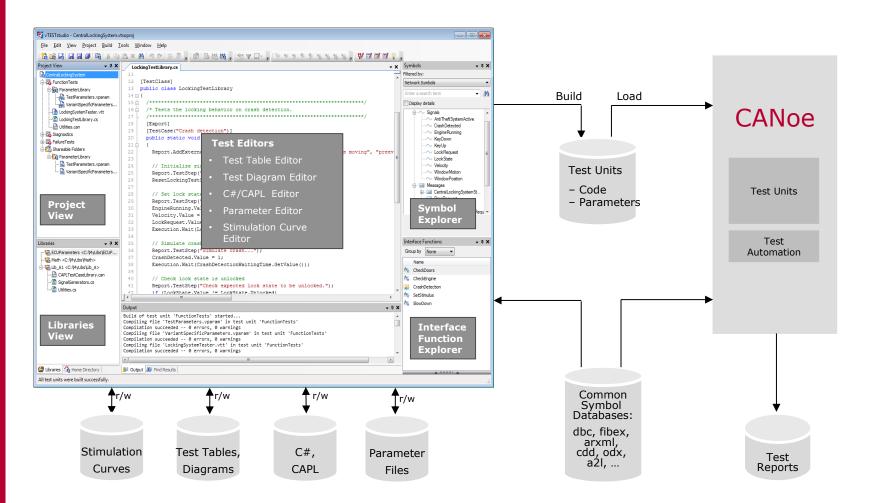
### Comfortable Design of Automated Test Sequences for Embedded Systems

- ▶ Vector vTESTstudio is a test design environment that enables the user
  - ▶ to simplify test design by combining different programming languages and graphical notations in one integrated design environment
  - ▶ to efficiently increase test coverage by specific test design features
  - to define and reuse test cases for and across product lines by supporting parameters, ECU variants and test variants
  - ▶ to track test coverage from system requirements to test reports





### Schematic Overview: vTESTstudio and CANoe

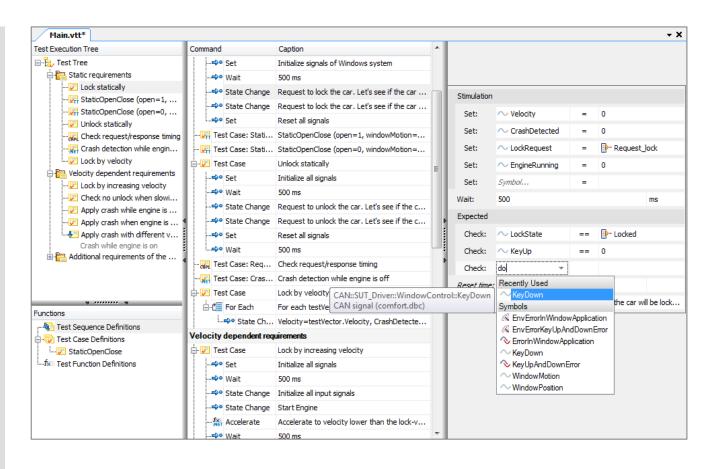




### vTESTstudio: Test Table Editor

#### Test Table Editor:

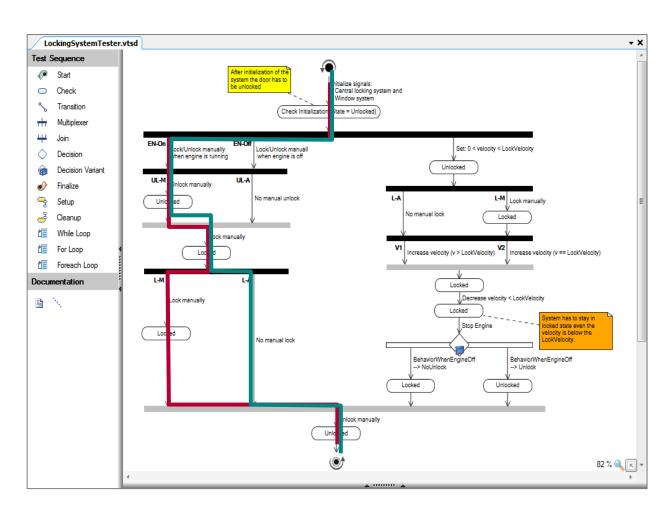
- Easily define test sequences without programming knowledge
- Comfortable support of test step parameterizati on by drag & drop
- Direct calls to CAPL, and C# test cases and functions possible





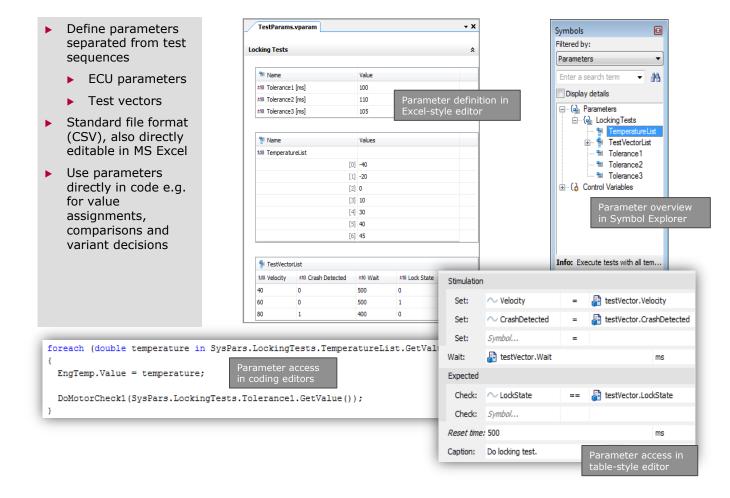
### vTESTstudio: Test Diagram Editor

- Test sequence diagram for an clear and concise representation
- Direct calls to CAPL and C# test cases and functions possible
- Test coverage easily to be reviewed
- Easy reuse of test sequence parts





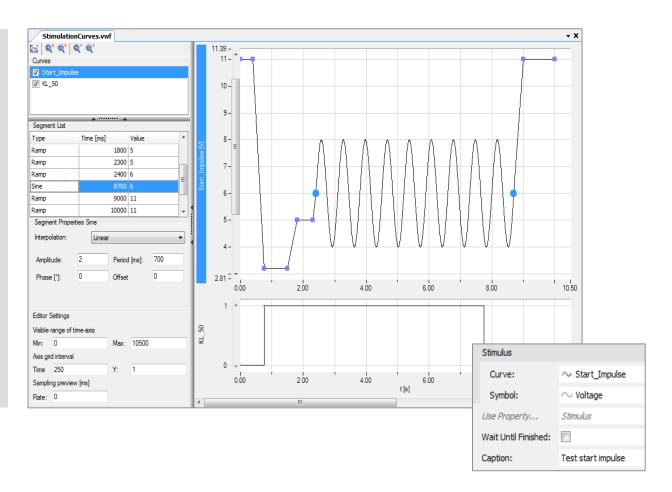
### vTESTstudio: Parameters and Test Vector Editors





### vTESTstudio: Stimulation Curve Editor

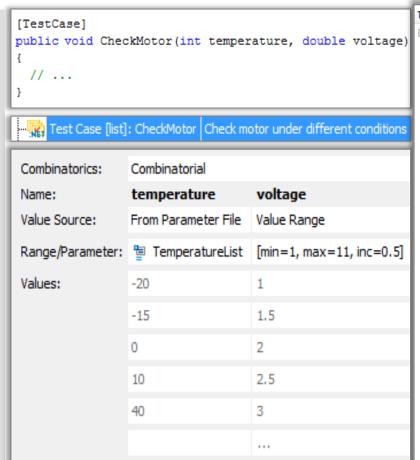
- Define curves graphically with the Waveform Editor
- Predefined segment types (sinus, pulse, ...) enable easy definition of e.g. voltage curves defined by test standards like LV124 norm
- Use curves for the stimulation of the system under test
- Multiple curves can easily be synchronized within the design and for test execution

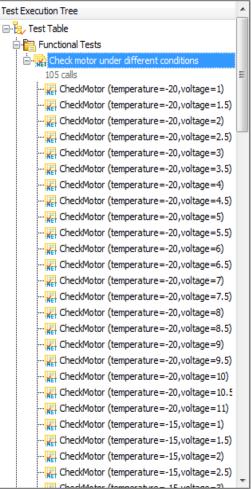




#### vTESTstudio: Parameterized Test Case Lists

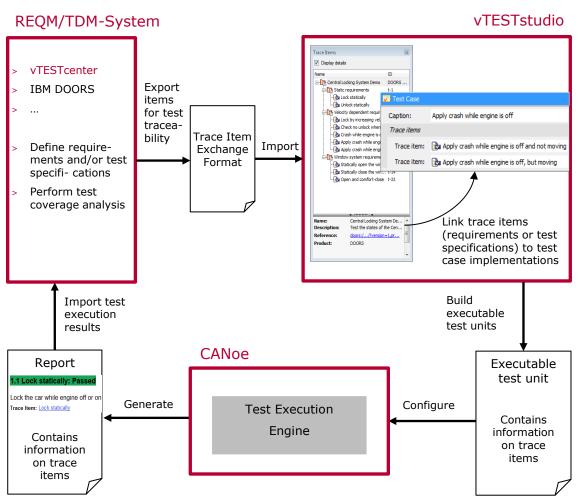
- Quick definition of a large number of test cases to increase test coverage
- Combinations
  - Sequential
  - All possible permutations
- Value definitions
  - Lists of values
  - Value ranges
- Direct use of parameters from parameter files as test case parameters





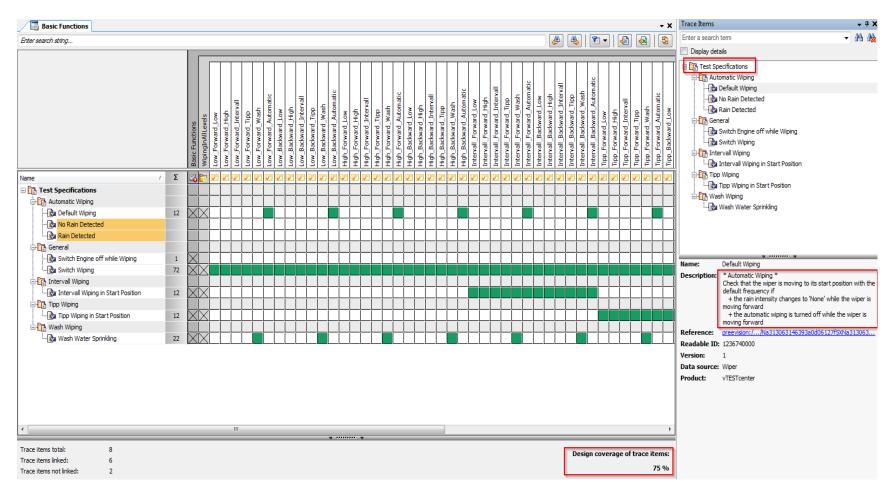


### vTESTstudio: Traceability





## vTESTstudio: Traceability Matrix





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Testing Engineering Services

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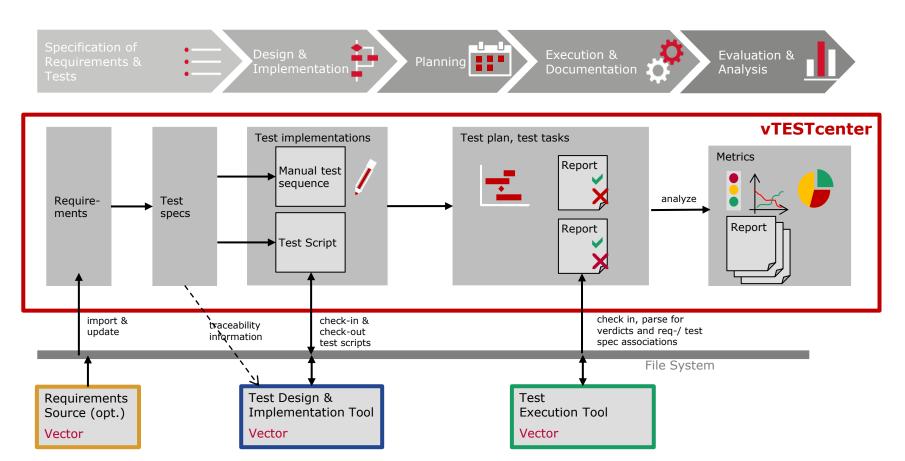


### Major Market Challenges

- Tons of data:
  - ▶ 1000's of requirements,
  - 100's of test scripts,
  - many SUT versions, many test executions
  - Allow / detect changes
- Address relevant roles
  - ▶ Stakeholders external & internal: Customers, Requirements Engineers, Management
  - Project members: Project leaders, Test Designers, Testers, Quality Engineers, ...
  - ▶ External contributors: Suppliers, service providers, ...
- Collaboration all users access the same data
  - Consider roles & rights to derive permissions for viewing / editing
  - Different locations
  - Configuration management with versioning
- Tracking of progress & quality
  - ▶ Review & release of each data element (→ 'artifact')
  - Informative reports with flexible level of details, graphical representations

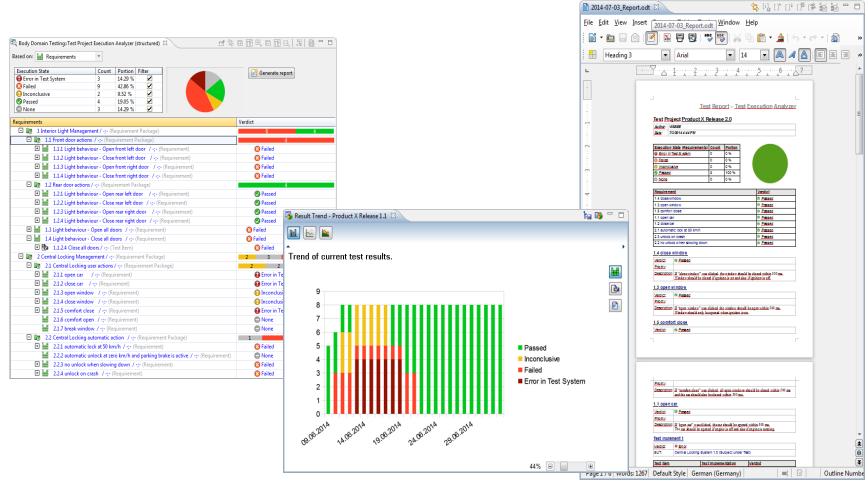


### vTESTcenter: Information Flow





vTESTcenter: Example Analysis of Test Results





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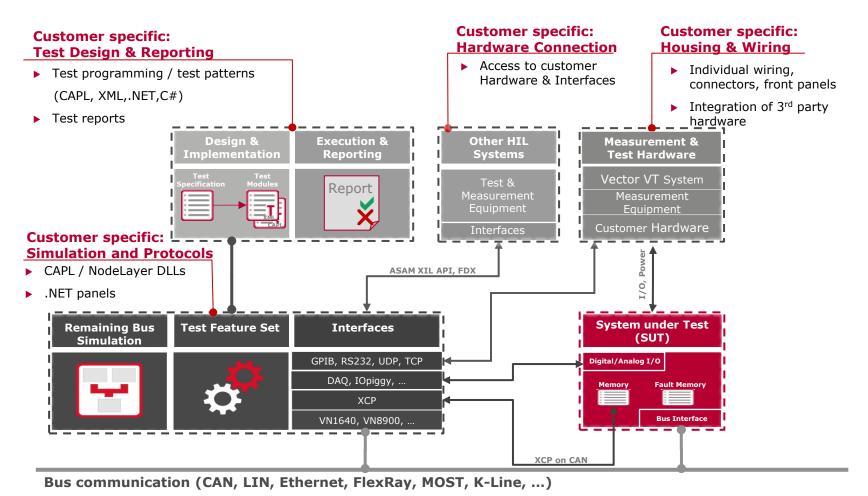
Test Data Management: vTESTcenter

Testing Engineering Services

Summary



### Range of Engineering Services





### Types of Tests

### We support you with various kinds of tests

- Functional Tests of ECUs with stimulation / measurement of IOs
  - Integration of network communication and electronic signals
- Gateway Tests
  - > Routing of messages and signals
- Diagnostic Tests (ECU functional behavior)
  - > E.g. check of fault memory entries
- Conformance Tests
  - For standard protocols, based on OEM specific specifications





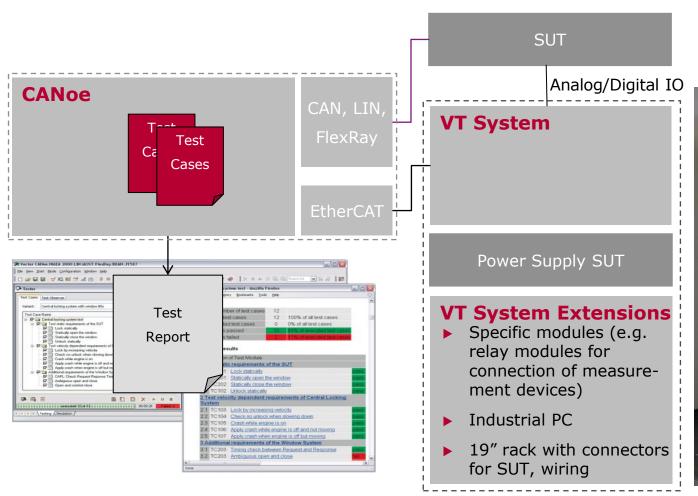


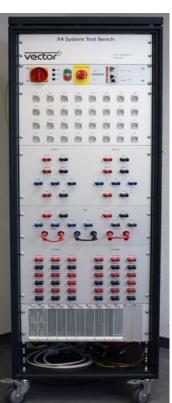
### For various application areas

- Interactive Tests
  - > With graphical user interface
- Automated Tests
  - > With monitoring of test progress
- Endurance Tests
  - > E.g. with different environmental conditions (control of climate chamber)



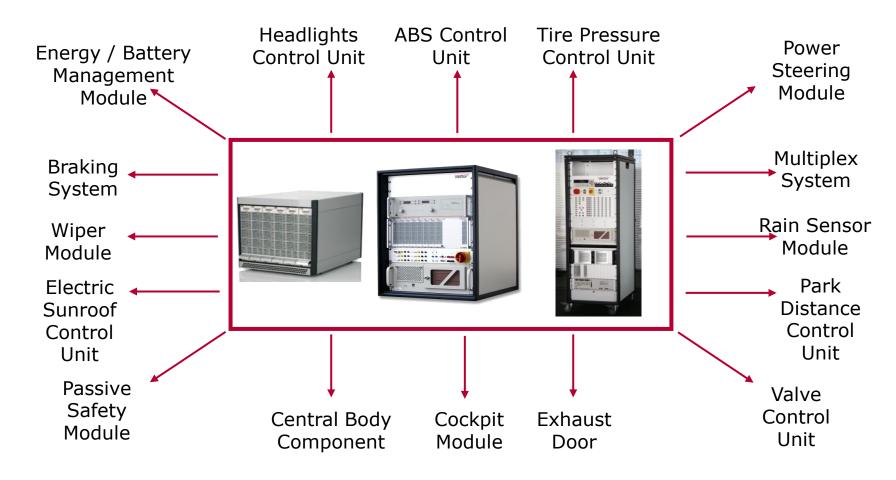
## Functional ECU Tests with VT System





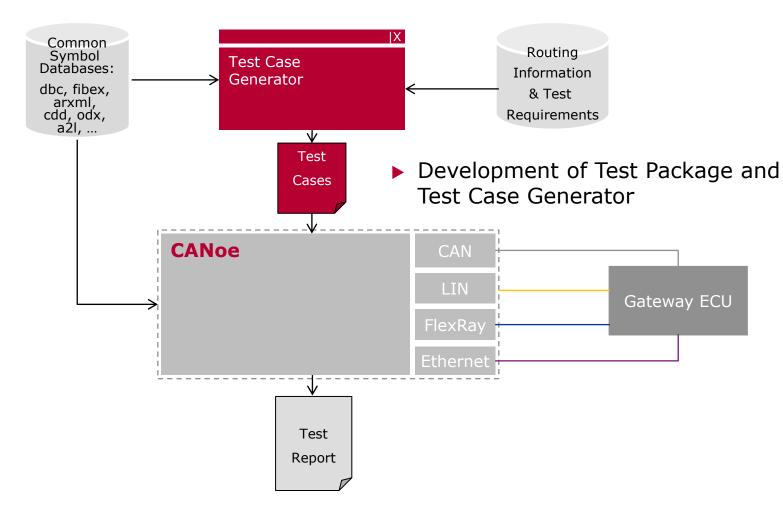


VT System Based Test Solutions For...



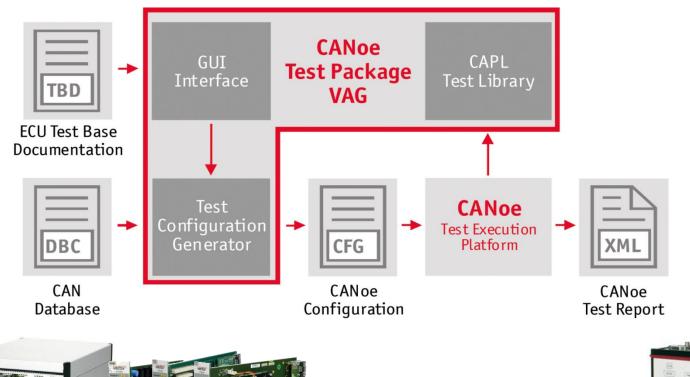


## Gateway Tests / Conformance Tests





### Example VAG Test Package Cover VW80118 & VW80119



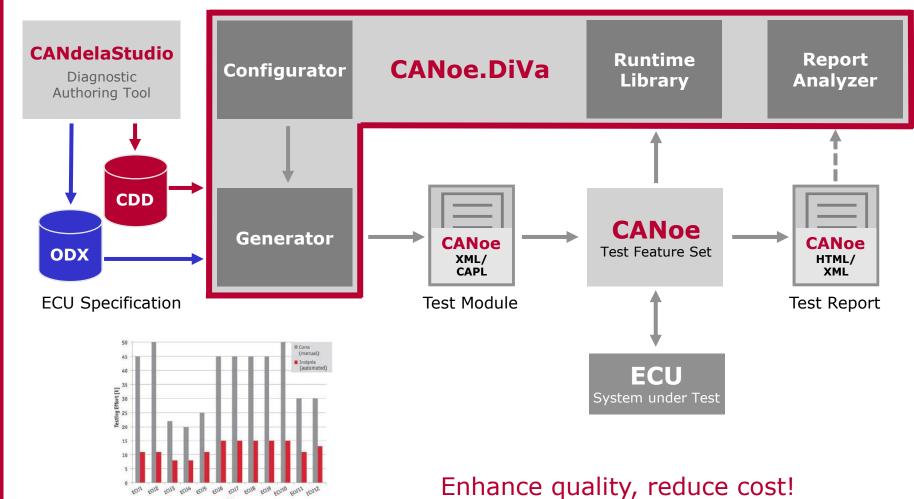






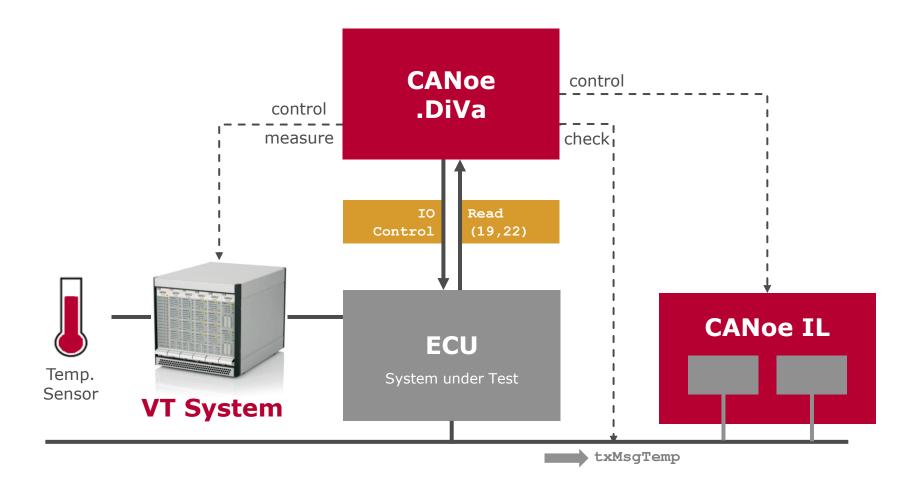


### Automated Test Process With CANoe.DiVa



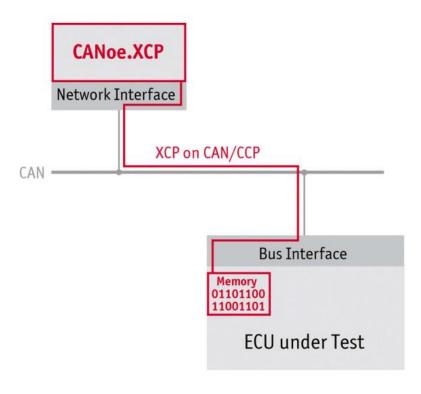


### Automated Test Process With CANoe.DiVa



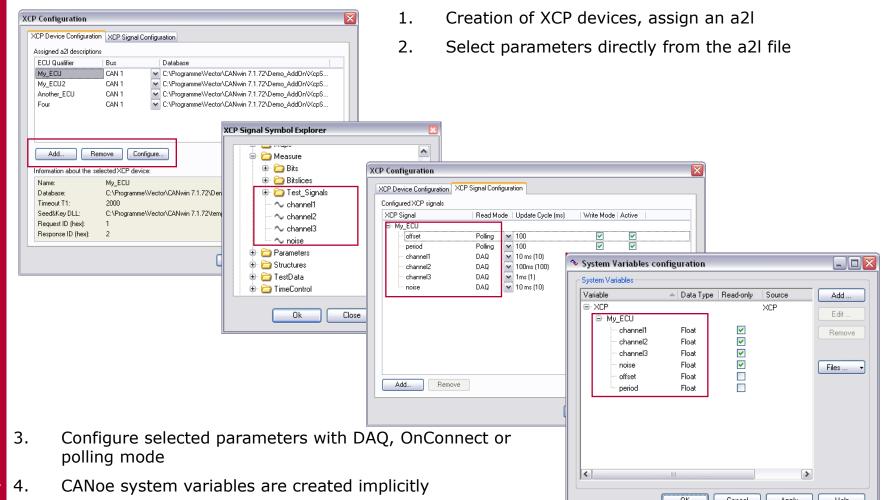


# CANoe.XCP - Comprehensive ECU Access for Testing and Analysis





# CANoe.XCP - Comprehensive ECU Access for Testing and Analysis



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# Test bench with VT System example









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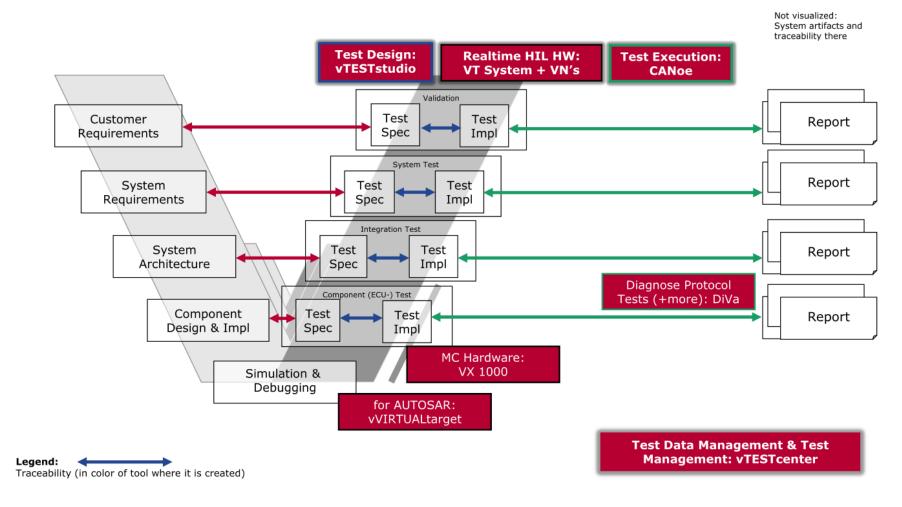
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Testing Engineering Services

Summary



### Vector Test Solution in the V-Model





## Visit our Website:

vector.com/vTESTcenter vector.com/vTESTstudio vector.com/VTsystem vector.com/CANoe

Author:

Kevin Fan (范科发)

Vector China